

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	<b>Address Error Detection by Merging a Polynomial-Based CRC Code of Address bits with Two Nibbles of Data or Data ECC Bits</b>
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Application Number :

Confirmation Number:

First Named Applicant: Kevin Normoyle

Attorney Docket Number: AZ-100

Art Unit:

Examiner:

Search string: ( 6574774 or 6539504 or 6480975 or 6308297 or 6134699 or 6003144 or 5978953 or 4672609 or 6457154 or 6457067 or 5768294 or 5761221 or 5691996 or 5226043 or 3963908 or 3231858 or 5841795 or 5345582 or 5099484 or 20030218816 or 20020007476 ),pn

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6574774	2003-06-03	Vasiliev	B1	714	800
	2	6539504	2003-03-25	Knefel	B1	714	718
	3	6480975	2002-11-12	Arimilli , et al.	B1	714	52
	4	6308297	2001-10-23	Harris	B1	714	773
	5	6134699	2000-10-17	Steenburgh , et al.	B1	714	803
	6	6003144	1999-12-14	Olarig , et al.	B1	714	42
	7	5978953	1999-11-02	Olarig	B1	714	768
	8	4672609	1987-06-09	Humphrey , et al.	B1	714	719
	9	6457154	2002-09-24	Chen , et al.	B1	714	768
	10	6457067	2002-09-24	Byers , et al.	B1	710	3;714
	11	5768294	1998-06-16	Chen , et al.	B1	714	766
	12	5761221	1998-06-02	Baat , et al.	B1	714	767
	13	5691996	1997-11-25	Chen , et al.	B1	714	805
	14	5226043	1993-07-06	Pughe, Jr. , et al.	B1	714	768
	15	3963908	1976-06-15	Das	B1	714	805
	16	3231858	1966-01-25	Tuomenoksa et al.	B1	714	805
	17	5841795	1998-11-24	Olarig , et al.	B1	714	768
	18	5345582	1994-09-06	Tsuchiya	B1	714	53
	19	5099484	1992-03-24	Smelser	B1	714	761

## US Published Applications

**Note: Applicant is not required to submit a paper copy of cited US Published Applications**

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030218816	2003-11-27	Katoh, Katsuhiko ; et al.	B1	360	53
	2	20020007476	2002-01-17	KISHINO, TSUYOSHI	B1	714	763

## Signature

Examiner Name	Date